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CONFIRMATION NO. 8930

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|-----------------------------|---------------------------------------|---------------------|------------------------|------------------------------------|
| SERIAL NUMBER<br>10/074,455 | FILING DATE<br>02/11/2002<br><br>RULE | CLASS<br>703<br>712 | GROUP ART UNIT<br>2125 | ATTORNEY<br>DOCKET NO.<br>5444P004 |
|-----------------------------|---------------------------------------|---------------------|------------------------|------------------------------------|

APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *Qun*  
 This appln claims benefit of 60/268,827 02/13/2001

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *Qun*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\* SMALL ENTITY \*\*  
 \*\* 03/08/2002

|   |   |                           |                        |                       |                            |
|---|---|---------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br><input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after | STATE OR<br>COUNTRY<br>CA | SHEETS<br>DRAWING<br>9 | TOTAL<br>CLAIMS<br>23 | INDEPENDENT<br>CLAIMS<br>3 |
|---|---|---------------------------|------------------------|-----------------------|----------------------------|

Verified and Acknowledged *Allowance*  
 Examiner's Signature *W. J. A.* Initials

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TITLE  
 Matched instruction set processor systems and method, system, and apparatus to efficiently design and implement matched instruction set processor systems by mapping system designs to re-configurable hardware platforms

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|----------------------------|---|--|
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